## **SIEMENS**

## **Data sheet**

## 6ES7332-5HF00-0AB0



SIMATIC S7-300, Analog output SM 332, isolated, 8 AO, U/I; diagnostics; resolution 11/12 bits, 40-pole, removing and inserting possible with active backplane bus

Figure similar

Figure similar	
Supply voltage	
Load voltage L+	
<ul><li>Rated value (DC)</li></ul>	24 V
<ul> <li>Reverse polarity protection</li> </ul>	Yes
Input current	
from load voltage L+ (without load), max.	340 mA
from backplane bus 5 V DC, max.	100 mA
Power loss	
Power loss, typ.	6 W
Analog outputs	
Number of analog outputs	8
Voltage output, short-circuit protection	Yes
Voltage output, short-circuit current, max.	25 mA
Current output, no-load voltage, max.	18 V
Output ranges, voltage	
• 0 to 10 V	Yes
• 1 V to 5 V	Yes
• -10 V to +10 V	Yes
Output ranges, current	
• 0 to 20 mA	Yes
• -20 mA to +20 mA	Yes
• 4 mA to 20 mA	Yes
Load impedance (in rated range of output)	
<ul><li>with voltage outputs, min.</li></ul>	1 kΩ
<ul> <li>with voltage outputs, capacitive load, max.</li> </ul>	1 μF
<ul> <li>with current outputs, max.</li> </ul>	500 Ω
<ul> <li>with current outputs, inductive load, max.</li> </ul>	10 mH
Cable length	
• shielded, max.	200 m
Analog value generation for the outputs	
Integration and conversion time/resolution per channel	
• Resolution with overrange (bit including sign), max.	12 bit; $\pm 10$ V, $\pm 20$ mA, 4 mA to 20 mA, 1 V to 5 V: 11 bit + sign; 0 V to 10 V, 0 mA to 20 mA: 12 bit
Conversion time (per channel)	0.8 ms
Settling time	
<ul> <li>for resistive load</li> </ul>	0.2 ms
for capacitive load	3.3 ms
for inductive load	0.5 ms; 0.5 ms (1 mH); 3.3 ms (10 mH)
Errors/accuracies	

0.5 %
0.6 %
0.4 %
0.5 %
Yes; Parameterizable
Yes; Parameterizable
Yes
Yes
No
Yes
Yes
Yes
500 V DC
40-pin
40 mm
125 mm
117 mm

last modified:

